FACULTY PROFILE

Name	: Mrs. Sangeeta Parshionikar
Designation	: Assistant Professor
Qualification	: ME (Electronics)
Email	: sangeeta@frcrce.ac.in
Areas Of Interest	: Digital Design Systems, VLSI Design, ASIC Verification, BEE, Computer Organization and architecture

PROFESSIONAL BACKGROUND

Teaching Experience:

July 2014 –till date	Fr.CRCE, Mumbai	Assistant professor
2007 – 2014	Fr.CRCE, Mumbai	Lecturer
2000 – 2001	Shri RKNEC, Nagpur	Lecturer
1999 - 2000	LAD College, Nagpur	Lecturer

VLSI Design Industry Experience:

March 2006 – June 2007	PowaiLabs Pvt. Ltd., Mumbai	Product Testing Team Lead
Sep'2005 – Dec'2005	Taray Technology Pvt. Ltd., Hyderabad	Design Engineer
June'2004 – Aug'2005	STRATeL Microsystems Pvt. Ltd., Delhi	Member of Technical Team

EDUCATIONAL DETAILS

BE (Electronics and Power Engg**.)**	Nagpur University	July 1999
ME **(**Electronics**)**	Mumbai University	March 2014

ADMINISTRATIVE BACKGROUND:

- BE Project Coordinator (Department of Electronics) since 2013 till date
- Convocation Coordinator 2018 -19
- Convocation Co - coordinator: 2016-17, 2017 -18
- Registration Committee Coordinator ICAC3 2019, Track Incharge, Program committee, Brochure and Poster ICAC3, 2017
- Laboratory Incharge Digital Design and Communication Lab

Ph.D. SUPERVISED : Nil

PUBLICATIONS:

International Conference:

- o Sangeeta Parshionikar, Siddheshwar Chiliveri, Aruna Anantharaj, Pratik Singh, "Real time monitoring of CO_2, TVOC, PM2.5, PM10 to evaluate Indoor Air quality and methods to control pollutants" at ICISS SCAD Institute of Technology, Coimbatore, IEEE Xplore 978-1-5386-7799-5

- o Trupti Shah. Sangeeta Parshionikar "Efficient Portable Camera Based Text to Speech Converter for Blind Person" at ICISS SCAD Institute of Technology, Coimbatore, IEEE Xplore 978-1-5386-7799-5.

- o Sangeeta Parshionikar, Archna Lopes, Heena Pendhari, "Generating Hardware Fingerprint of FPGA using physically unclonable function", ICRIEECE, Bhubhneshwar, Odissa, 27th July 2018

Journals:

- o Archana Lopes, Sangeeta Parshionikar, Heena Pendhari "Early Detection Of Heart Disease Using Brann", IJCRT Volume 6, Issue 2 April 2018 | ISSN: 2320-2882, Pg No 741 -745.

- o PushkarVaity, Manali Patil, Sonali Dhage, Sangeeta Parshionikar "Design and FPGA Implementation of High Speed UART", International Journal of Scientific and Engineering Research (IJSER) Volume 7, Issue 9, September 2016 Edition (ISSN 2229-5518) Pg no 106 - 110.

- o Mansi Warde, Sangeeta Parshionikar "Design and analysis of 8×8 Static RAM", International Journal of Scientific and Engineering Research (IJSER) Volume 6, Issue 7, July 2015 Edition (ISSN 2229-5518) Pg no 338 - 342

- o Sangeeta Parshionikar, Dr. D.V.Bhoir, Sapna Prabhu "Leakage Power Reduction using Multi Threshold Voltage CMOS Technique", International Journal of Scientific and Engineering Research (IJSER) Volume 4, Issue10, October 2013 Edition (ISSN 2229-5518) Pg no 1077 - 1080

- o Sangeeta Parshionikar, Dr.D.V.Bhoir,"Standby leakage power reduction techniques in Deep Submicron CMOS VLSI circuits", IJCA and presented the same in International Conference on Communication Technology ICCT held in D.J.Sanghvi College of Engineering, October 2013

MINOR RESEARCH PROJECTS:

- ➢ Smart Air Pollution Monitoring system in Urban Environment
- ➢ Smart Soil analyzer using IoT
- ➢ FPGA realization of BPNN for stock market prediction
- ➢ Smart Agriculture System (e-Krishirakshak)

BOOKS AUTHORED : Nil

TRAINING PROGRAMS CONDUCTED :

1. Conducted a Webinar on "RTL Design and Functional Verification", July 27, 2018.

2. Co-ordinated two day faculty development program on "Hands on FPGA Applications" organized by department of Electronics, CRCE, from 23rd and 24th Feb 2018.

3. Co-ordinated two day faculty development program on "IC technology: Layout to Simulation" organized by department of Electronics, CRCE, from 4th& 5th Sep 2015.

4. Co-ordinated two day faculty development program on "Challenges in VLSI Design" organized by department of Electronics, CRCE on 13th – 14th March 2015.

5. Co-ordinated two day faculty development program on "Industrial Automation" organized by department of Electronics, CRCE on 11th – 12th March 2010.

PARTICIPATION IN TRAINING PROGRAM   :
1. One week ISTE STTP on "IoT with Arduino and RaspberryPi", at Xavier IE, Mumbai, July 02 – 06, 2018.
2. 8 weeks NPTEL online course on "Introduction to Reseach", February, 2017– April , 2017
3. 8 weeks NPTEL online course on "Enhancing Soft skill & Personality", Feb 20, 2017 – April 14, 2017
4. One week FDP on" ASIC Verification with System Verilog", at SPIT Andheri, Dec 21 – 26, 2015.
5. ISTE approved one week STTP on "Accreditation & Quality Assurance, Outcome based Education", at Fr. CRCE, Bandra, Nov 17 – 21, 2014.
6. AICTE sponsored National level STTP on "VLSI and Embedded Systems" at Fr. CRIT Vashi, July 01 - 05, 2013.
7. One week ISTE STTP on "Tools and techniques for effective technical writing" at K.J. Somaiya College of Engg.Vidyavihar, May 13 -17, 2013.
8. Two week graded STTP on "Basics Electronics" conducted by IIT, Bombay,July 2011.
9. One week STTP on "Image Processing Applications :Basics to Advanced" at St. Francis Institute of Technology in January 2010.
10. One week STTP on "Electromagnetic Waves and Its Applications", at SPIT in June 2009.
11. Two week STTP on "Robotics and its Applications", at Fr. CRCE in Dec. 2007.

PROFESSIONAL AFFILIATIONS   : ISTE Life Member